

SHEET INDEX

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CONTENTS

SHEET NO.

ISSUE NO.

DATE	CD	DATE	DATE	DATE
1	1	1	1	1
2D	APPX	1	1	1
ID	ID	1	1	1

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

SUPPORTING INFORMATION

CATEGORY	NO.
CIRCUIT PACK SCHEMATICS	CP-303 CP-312
EQUIPMENT DRAWING	J1025B
DC-DC CONVERTER (J97421A)	SD-B2327-01
DC-DC CONVERTER (J97422B)	SD-B2328-02

NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

REPLACES SD-10903-01

COMMON SYSTEMS
MAIN STORE MEMORY
CIRCUITS

AT&T
STANDARD

SD-IC903-02-A1
30 SHEETS

BELL TELEPHONE LABORATORIES
INCORPORATED

2
65

APPARATUS INDEX

A	P/P FIGURE			LOCATION			
	EQUIP	LOC	NO., SH.	DESIG	FS/SVP	APPG	EDPT
	CIRCUIT PACKS			CIRCUIT PACK-CP			
	03-02	2	C1	FOB0	1/1	2	03-02
	03-04	2	C1	FOB1	1/15	4	03-23
	03-05	2	C1	MP09H.PL	1/2	2	03-17
	03-06	2	C1	MP10H.PL	1/16	4	03-18
B	03-07	2	C1	MP008.00	1/14	2	03-04
	03-08	2	C1	MP009.01	1/13	2	03-05
	03-09	2	C1	MP010.02	1/12	2	03-06
	03-10	2	C1	MP011.03	1/11	2	03-07
	03-11	2	C1	MP012.04	1/10	2	03-08
	03-12	3	C1	MP013.05	1/9	2	03-09
	03-13	3	C1	MP014.06	1/8	2	03-10
	03-14	3	C1	MP015.07	1/7	2	03-11
	03-16	3	C1	MP020.16	1/6	3	03-12
	03-17	2	C1	MP021.17	1/5	3	03-13
	03-18	4	C1	MP022.18	1/4	3	03-14
	03-19	5	C2	MP023.19	1/3	3	03-16
C	03-20	5	C2	MP108.00	1/28	4	03-31
	03-21	5	C2	MP109.01	1/27	4	03-29
	03-22	5	C2	MP110.02	1/26	4	03-28
	03-23	4	C1	MP111.03	1/25	4	03-27
	03-24	4	C1	MP112.04	1/24	4	03-26
	03-25	4	C1	MP113.05	1/23	4	03-25
	03-26	4	C1	MP114.06	1/22	4	03-24
	03-27	4	C1	MP115.07	1/21	4	03-23
D	03-28	4	C1	MP120.16	1/20	5	03-22
	03-29	4	C1	MP121.17	1/19	5	03-21
	03-31	4	C1	MP122.18	1/18	5	03-20
	03-33	4	C1	MP123.19	1/17	5	03-19
DESIG				POWER MODULE			
	FOB0	2	C1	+5VPH	2/2	1	03-38
	FOB1	4	C1	SVPH	2/1	1	03-44
	MP09H.PL	2	C1				
	MP10H.PL	4	C1				
E	MP008.00	2	C1				
	MP009.01	-	C1				
	MP010.02	2	C1				
	MP011.03	2	C1				
	MP012.04	2	C1				
	MP013.05	2	C1				
	MP014.06	2	C1				
	MP015.07	2	C1				
F	MP020.16	3	C1				
	MP021.17	3	C1				
	MP022.18	3	C1				
	MP023.19	3	C1				
	MP108.00	4	C1				
	MP109.01	4	C1				
	MP110.02	4	C1				
	MP111.03	4	C1				
	MP112.04	4	C1				
	MP113.05	4	C1				
	MP114.06	4	C1				
	MP115.07	4	C1				
G	MP120.16	5	C2				
	MP121.17	5	C2				
	MP122.18	5	C2				
	MP123.19	5	C2				

ISSUE
1

IV IN STORE MEMORY

SD-1C903-02-A2

BELL TELEPHONE LABORATORIES
(INCORPORATED)

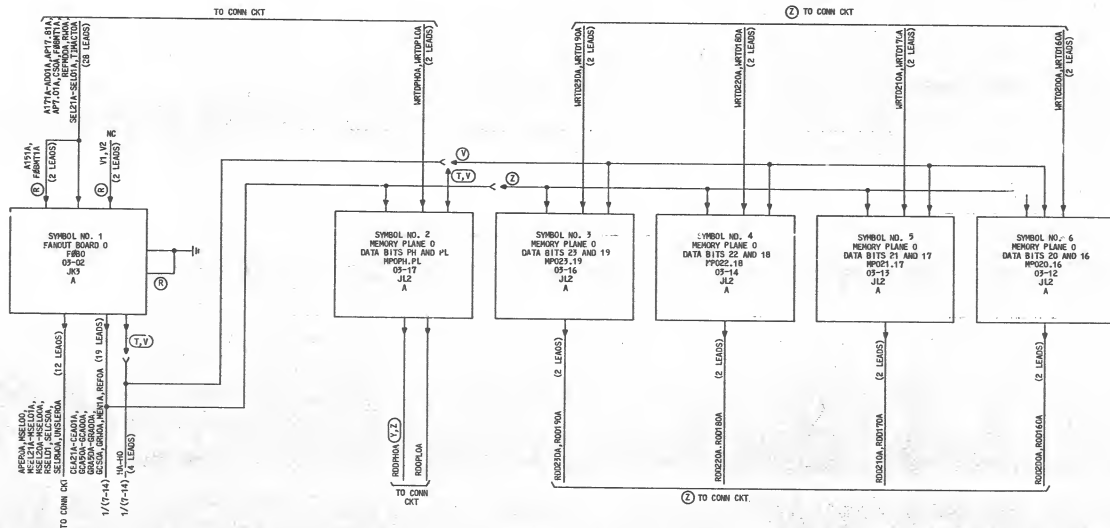
REV. 1/78
C2

SD-1C903-02-A2

OPTION INDEX			
APP OR MRS	RATED ON ISSUE	REF NOTES	LOCATION
Z	STD 2		1/(1,3-6), 1/(15,17-20)
Y	STD 2		1/2
V	STD 2		1/(1-20)
T	STD 2		1/(1,2,7-14), 1/(15,16,21-28)
S	PD 2		2/(3-8)
R	STD 2		1/(1,15), 2/(8,10)

MAIN STORE HENRY		2	ORG SIZE 65	ISSUE 2D
BELL LABORATORIES		SD-1C903-02		A3

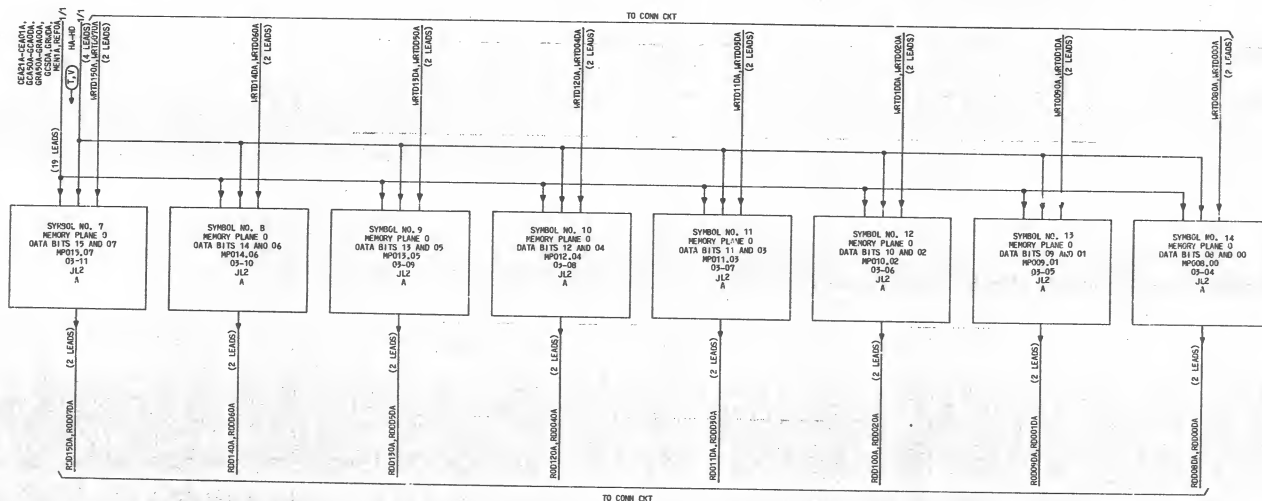
MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE MEMORY		DWG SIZE	ISSUE
2		A5	2D
BELL LABORATORIES	SD-IC903-02		BIAA

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)

PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE MEMORY

DWG SIZE
65

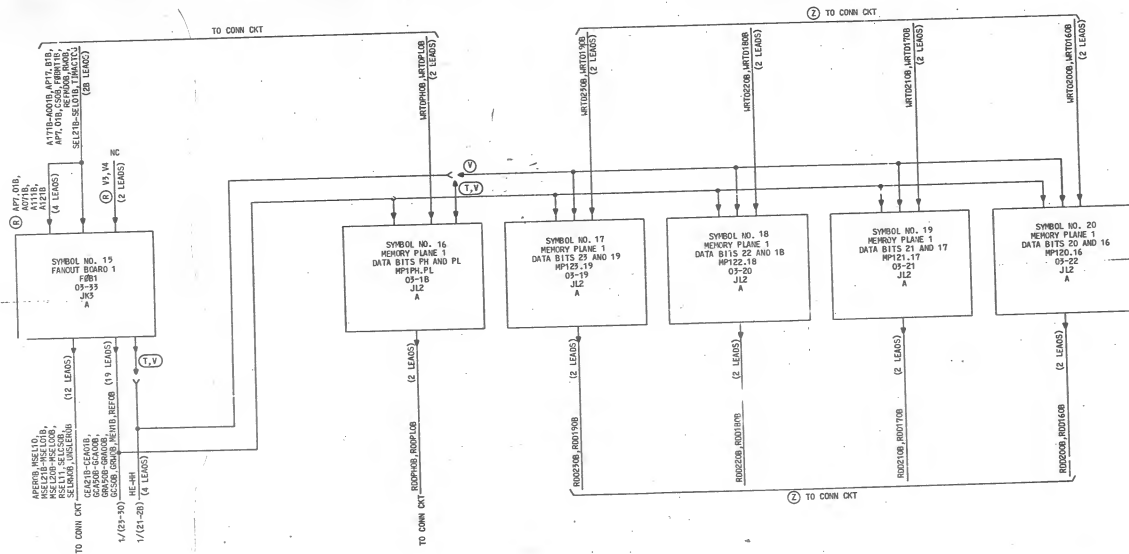
ISSUE
20

BELL LABORATORIES

SD-IC903-02

RIAR

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE MEMORY

DWG SI

65

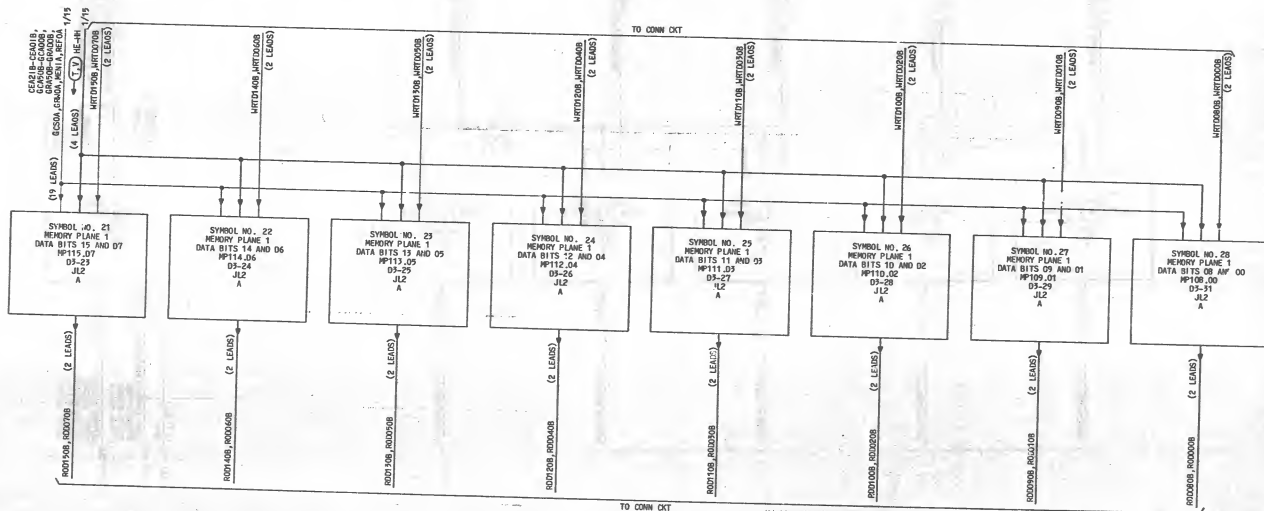
BELL LABORATORIES

SD-IC903-02

BIAC

PART OF FS I

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)

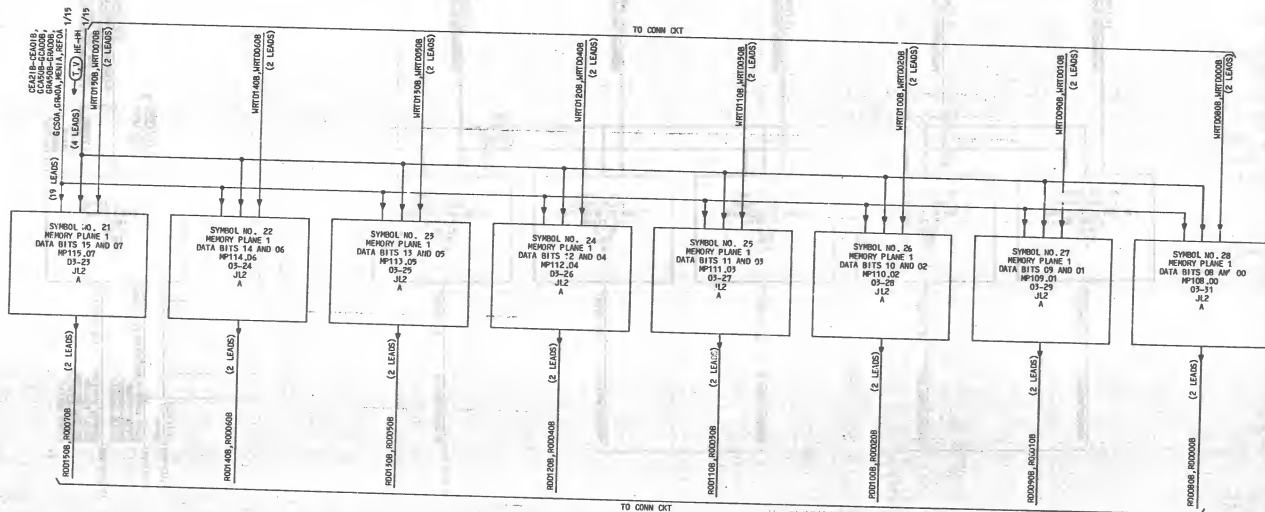


PART OF FS I
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE MEMORY		ONE SIZE 45	ISSUE 2D
BELL LABORATORIES		SD-1C903-02	BIAD

PART OF FS 1

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



PART OF FS 1
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE MEMORY		ORG SIZE	ISSUE
		65	2D
BELL LABORATORIES	SD-IC903-02	BIAD	

MEMORY
INTERCONNECTION AND FLOW DIAGRAM
(SEE COMPOSITE DIAGRAM 1)



MAIN STORE MEMORY

OWG S
AS

ISSUE
3D

BELL LABORATORIES

SD-IC903-02

BIAD

PART OF FS 1

MEMORY

SYMBOL/LEAD DESIGNATION

SYMBOL/LEAD DESIGNATION	DEFINITION
MPZM01C(A,B)	ADDRESS PARITY ERROR
AP17,811(A,B)	ADDRESS PARITY 17 THROUGH 8 (PARITY OVER ADDRESS BITS 8-17 TO FANOUT BOARD FROM MAIN STORE CONTROLLER)
AP7,01(A,B)	ADDRESS PARITY 7 THROUGH 1 (PARITY OVER ADDRESS BITS 1-7 TO FANOUT BOARD FROM MAIN STORE CONTROLLER)
A(00-17)1(A,B)	ADDRESS (00-17) (FROM MAIN STORE CONTROLLER TO FANOUT BOARD)
CEA(0-2)1(A,B)	CHIP ENABLE ADDRESS (0-2) (FANOUT BOARD TO MEMORY PLANE)
CS0(A,B)	CHIP SELECT (TO FANOUT FROM MAIN STORE CONTROLLER)
FBMT1(A,B)	FANOUT BOARD MAINTENANCE
GCA(0-5)0(A,B)	GATED COLUMN ADDRESS (0-5)
FTS0(A,B)	GATED CHIP SELECT
GRA(0-5)0(A,B)	GATED ROW ADDRESS (0-5)
GRDA	GROUND
GRW0(A,B)	GATED READ/WRITE
H(A-B)	SPARE
MEM1(A,B)	MEMORY ENABLE
MSEL(0,1)0	NOD SELECTED (0,1)
MSEL(00,01)1(A,B)	NOD SELECTED (HARD-WIRED ADDRESS OUTPUTS ON FANOUT BOARD)
MSEL(10,11)1(A,B)	NOD SELECTED (HARD-WIRED ADDRESS OUTPUTS ON FANOUT BOARD)
MSEL(20,21)1(A,B)	NOD SELECTED (HARD-WIRED ADDRESS OUTPUTS ON FANOUT BOARD)
RODPH0(A,B)	READ DATA PARITY HIGH
RODPL0(A,B)	READ DATA PARITY LOW
RD0(00-23)0(A,B)	READ DATA (0-23)
REFPH0(A,B)	REFRESH HIGH
REF0(A,B)	REFRESH
RSEL(0,1)1	REFRESH SELECT
RWD(A,B)	READ/WRITE
SELC0(A,B)	SELECTED CHIP SELECT (SANITY INDICATOR FROM FANOUT BOARD)
SELRW0(A,B)	SELECTED READ/WRITE (SANITY INDICATOR FROM FANOUT BOARD)
TIMACT0(A,B)	TIMING ACTIVE
UNSELECT0(A,B)	UNSELECTED ERROR
WRDPH0(A,B)	WRITE DATA PARITY HIGH
WRDPL0(A,B)	WRITE DATA PARITY LOW
WR0(00-23)0(A,B)	WRITE DATA (0-23)
OGROUND	GROUND
SEL(0-2)1(A,B)	SFLECT (HARD-WIRED MODULE SELECT ADDRESS INPUTS ON FANOUT BOARD)
V(1-4)	SPARE

MAIN STORE MEMORY		ONG SIZE C2	ISSUE 20
BELL LABORATORIES	SD-1C903-02		81AE
PRINTED IN U. S. A.		05/23/77	

PART OF FS 1

SYMBOL NO. 3

MEMORY PLANE 0
DATA BITS 23 AND 19

DESIG	EOPT	LOC	CODE	ELEM	IDENT	OPT
MP023.19	03-16	JL2		A		

FS INFO

LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM.	LOC
-SV	CEA011	PUR	019	1/1		380	
CEA01A	1	202	1/1			380	
CEA11A	1	009	1/1			200	
CEA21A	1	209	1/1			200	
CEA30A	1	206	1/1			200	
CEA10A	1	204	1/1			200	
CEA20A	1	004	1/1			200	
CEA30A	1	006	1/1			200	
CEA40A	1	005	1/1			200	
CEA50A	1	207	1/1			200	
CEA60A	1	003	1/1			200	
CEA70A	1	007	1/1			200	
CEA80A	1	205	1/1			200	
CEA90A	1	006	1/1			200	
CEA10A	1	203	1/1			200	
CEA20A	1	201	1/1			200	
CEA30A	1	002	1/1			200	
CEA40A	1	001	1/1			200	
HA	1	018	(T,V)1/1				
HB	1	017	(T,V)1/1				
HC	1	016	(T,V)1/1				
HD	1	015	(T,V)1/1				
HE	1	014	(T,V)1/1				
HF	1	013	(T,V)1/1				
HG	1	012	(T,V)1/1				
HH	1	011	(T,V)1/1				
HI	1	010	(T,V)1/1				
HO	1	009	(T,V)1/1				
HP	1	008	(T,V)1/1				
HQ	1	007	(T,V)1/1				
HR	1	006	(T,V)1/1				
HS	1	005	(T,V)1/1				
HT	1	004	(T,V)1/1				
HU	1	003	(T,V)1/1				
HV	1	002	(T,V)1/1				
HW	1	001	(T,V)1/1				

SYMBOL NO. 4

MEMORY PLANE 0
DATA BITS 22 AND 18

DESIG	EOPT	LOC	CODE	ELEM	IDENT	OPT
MP022.18	03-14	JL2		A		

FS INFO

LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM.	LOC
-SV	CEA011	PUR	019	1/1		380	
CEA01A	1	202	1/1			380	
CEA11A	1	009	1/1			200	
CEA21A	1	209	1/1			200	
CEA30A	1	206	1/1			200	
CEA10A	1	204	1/1			200	
CEA20A	1	004	1/1			200	
CEA30A	1	006	1/1			200	
CEA40A	1	005	1/1			200	
CEA50A	1	207	1/1			200	
CEA60A	1	003	1/1			200	
CEA70A	1	007	1/1			200	
CEA80A	1	205	1/1			200	
CEA90A	1	006	1/1			200	
CEA10A	1	203	1/1			200	
CEA20A	1	201	1/1			200	
CEA30A	1	002	1/1			200	
CEA40A	1	001	1/1			200	
HA	1	018	(T,V)1/1				
HB	1	017	(T,V)1/1				
HC	1	016	(T,V)1/1				
HD	1	015	(T,V)1/1				
HE	1	014	(T,V)1/1				
HF	1	013	(T,V)1/1				
HG	1	012	(T,V)1/1				
HH	1	011	(T,V)1/1				
HI	1	010	(T,V)1/1				
HO	1	009	(T,V)1/1				
HP	1	008	(T,V)1/1				
HQ	1	007	(T,V)1/1				
HR	1	006	(T,V)1/1				
HS	1	005	(T,V)1/1				
HT	1	004	(T,V)1/1				
HU	1	003	(T,V)1/1				
HV	1	002	(T,V)1/1				
HW	1	001	(T,V)1/1				

SYMBOL NO. 5

MEMORY PLANE 0
DATA BITS 21 AND 17

DESIG	EOPT	LOC	CODE	ELEM	IDENT	OPT
MP021.17	03-13	JL2		A		

FS INFO

LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM.	LOC
-SV	CEA011	PUR	019	1/1		380	
CEA01A	1	202	1/1			380	
CEA11A	1	009	1/1			200	
CEA21A	1	209	1/1			200	
CEA30A	1	206	1/1			200	
CEA10A	1	204	1/1			200	
CEA20A	1	004	1/1			200	
CEA30A	1	006	1/1			200	
CEA40A	1	005	1/1			200	
CEA50A	1	207	1/1			200	
CEA60A	1	003	1/1			200	
CEA70A	1	007	1/1			200	
CEA80A	1	205	1/1			200	
CEA90A	1	006	1/1			200	
CEA10A	1	203	1/1			200	
CEA20A	1	201	1/1			200	
CEA30A	1	002	1/1			200	
CEA40A	1	001	1/1			200	
HA	1	018	(T,V)1/1				
HB	1	017	(T,V)1/1				
HC	1	016	(T,V)1/1				
HD	1	015	(T,V)1/1				
HE	1	014	(T,V)1/1				
HF	1	013	(T,V)1/1				
HG	1	012	(T,V)1/1				
HH	1	011	(T,V)1/1				
HI	1	010	(T,V)1/1				
HO	1	009	(T,V)1/1				
HP	1	008	(T,V)1/1				
HQ	1	007	(T,V)1/1				
HR	1	006	(T,V)1/1				
HS	1	005	(T,V)1/1				
HT	1	004	(T,V)1/1				
HU	1	003	(T,V)1/1				
HV	1	002	(T,V)1/1				
HW	1	001	(T,V)1/1				

SYMBOL NO. 6

MEMORY PLANE 0
DATA BITS 20 AND 16

DESIG	EOPT	LOC	CODE	ELEM	IDENT	OPT
MP020.16	03-12	JL2		A		

FS INFO

LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM.	LOC
-SV	CEA011	PUR	019	1/1		380	
CEA01A	1	202	1/1			380	
CEA11A	1	009	1/1			200	
CEA21A	1	209	1/1			200	
CEA30A	1	206	1/1			200	
CEA10A	1	204	1/1			200	
CEA20A	1	004	1/1			200	
CEA30A	1	006	1/1			200	
CEA40A	1	005	1/1			200	
CEA50A	1	207	1/1			200	
CEA60A	1	003	1/1			200	
CEA70A	1	007	1/1			200	
CEA80A	1	205	1/1			200	
CEA90A	1	006	1/1			200	
CEA10A	1	203	1/1			200	
CEA20A	1	201	1/1			200	
CEA30A	1	002	1/1			200	
CEA40A	1	001	1/1			200	
HA	1	018	(T,V)1/1				
HB	1	017	(T,V)1/1				
HC	1	016	(T,V)1/1				
HD	1	015	(T,V)1/1				
HE	1	014	(T,V)1/1				
HF	1	013	(T,V)1/1				
HG	1	012	(T,V)1/1				
HH	1	011	(T,V)1/1				
HI	1	010	(T,V)1/1				
HO	1	009	(T,V)1/1				
HP	1	008	(T,V)1/1				
HQ	1	007	(T,V)1/1				
HR	1	006	(T,V)1/1				
HS	1	005	(T,V)1/1				
HT	1	004	(T,V)1/1				
HU	1	003	(T,V)1/1				
HV	1	002	(T,V)1/1				
HW	1	001	(T,V)1/1				

PART OF FS 1

SYMBOL(S) 3 4 5 6

MAIN STORE MEMORY		ORG SIZE	1500E
		C2	20
BELL LABORATORIES		SD-1C903-02	81BC
		PRINTED IN U.S.A.	08/02/77

MEMORY

MEMORY PLANE 0
DATA BITS 15 AND 07

MEMORY PLANE 0
DATA BITS 14 AND 06

MEMORY PLANE D
DATA BITS 13 AND 05

MEMORY PLANE D
DATA BITS 12 AND 04

08702177

MEMORY

SYMBOL NO. 14

MEMORY PLANE 0

DATA BITS 08 AND 09

SYMBOL(S) 11 12 13 14

MAIN STORE MEMORY

DWG SIZE
C2

BELL LABORATORIES	SD-1C903-02
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	B1CD
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0676277

PART OF FS 1

MEMORY

SYMBOL NO. 15

FAULT BOARD 1

SYMBOL NO. 15 (CONT)

FAULT BOARD 1

SYMBOL NO. 15 (CONT)

FAULT BOARD 1

SYMBOL NO. 15 (CONT)

FAULT BOARD 1

A

DESIG	EOPT	CODE	ELEM	OPT	DESIG	EOPT	CODE	ELEM	OPT
FOBI	03-33	JK3	A		FOBI	03-33	JK3	A	
FS INFO					CP INFO				
LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM.	MOD	LOC	
APR018	I	212		TO CONN CKT		APR018	212	249	
APR018	I	303		TO CONN CKT		APR018	303	345	
APR018	I	204		TO CONN CKT		APR018	204	249	
AS018	I	222		TO CONN CKT		AS018	222	248	
AD118	I	112		TO CONN CKT		AD118	112	248	
AD218	I	011		TO CONN CKT		AD218	011	247	
AD318	I	111		TO CONN CKT		AD318	111	246	
AD418	I	207		TO CONN CKT		AD418	207	246	
AD518	I	012		TO CONN CKT		AD518	012	245	
AD618	I	306		TO CONN CKT		AD618	306	245	
AD718	I	208		TO CONN CKT		AD718	208	244	
AS018	I	215		TO CONN CKT		AS018	215	243	
AS018	I	309		TO CONN CKT		AS018	309	243	
AS018	I	307		TO CONN CKT		AS018	307	242	
A1018	I	305		TO CONN CKT		A1018	305	242	
A1118	I	206		TO CONN CKT		A1118	206	241	
A1218	I	110		TO CONN CKT		A1218	110	241	
A1318	I	213		TO CONN CKT		A1318	213	240	
A1418	I	308		TO CONN CKT		A1418	308	240	
A1518	I	209		TO CONN CKT		A1518	209	240	
A1618	I	016		TO CONN CKT		A1618	016	240	
A1718	I	117		TO CONN CKT		A1718	117	240	
A1718	I	019		TO CONN CKT		A1718	019	240	
CEA018	I	102		TO CONN CKT		CEA018	102	240	
CEA118	I	009		TO CONN CKT		CEA118	009	240	
CEA218	I	109		TO CONN CKT		CEA218	109	240	
CS018	I	201		TO CONN CKT		CS018	201	240	
CS018	I	216		TO CONN CKT		CS018	216	240	
GRA008	I	106		TO CONN CKT		GRA008	106	240	
GRA108	I	104		TO CONN CKT		GRA108	104	240	
GRA208	I	004		TO CONN CKT		GRA208	004	240	
GRA308	I	103		TO CONN CKT		GRA308	103	240	
GRA408	I	101		TO CONN CKT		GRA408	101	240	
GRA508	I	002		TO CONN CKT		GRA508	002	240	
GRA608	I	001		TO CONN CKT		GRA608	001	240	

DESIG	EOPT	CODE	ELEM	OPT	DESIG	EOPT	CODE	ELEM	OPT
FOBI	03-33	JK3	A		FOBI	03-33	JK3	A	
FS INFO					CP INFO				
LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM.	MOD	LOC	
GCAS08	I	008		TO CONN CKT		GCAS08	008	241	
GCAS08	I	005		TO CONN CKT		GCAS08	005	240	
GCAS08	I	107		TO CONN CKT		GCAS08	107	240	
GC508	I	003		TO CONN CKT		GC508	003	240	
GRA008	I	007		TO CONN CKT		GRA008	007	240	
GRA108	I	105		TO CONN CKT		GRA108	105	240	
GRA208	I	006		TO CONN CKT		GRA208	006	240	
GRA308	I	103		TO CONN CKT		GRA308	103	240	
GRA408	I	101		TO CONN CKT		GRA408	101	240	
GRA508	I	002		TO CONN CKT		GRA508	002	240	
GRA608	I	001		TO CONN CKT		GRA608	001	240	

DESIG	EOPT	LOC	CODE	ELEM	LOC	OPT	
FOBI	03-33		JK3	A			
FS INFO				CP INFO			
LEAD	DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. MOD	LOC
				1/26, 1/27 1/28, 1/27 1/28			
HE	I	318		001127, CT, V01116 001138, 001119 001209, CT, V01121 CT, V01122, CT, V01123 CT, V01124, CT, V01125 CT, V01126, CT, V01127 CT, V01128			
HF	I	317		001120, CT, V01119 001138, 001119 CT, V01122, CT, V01123 CT, V01124, CT, V01125 CT, V01126, CT, V01127 CT, V01128, CT, V01129 CT, V01128			
HG	I	316		001117, CT, V01116 001138, 001119 001120, CT, V01121 CT, V01122, CT, V01123 CT, V01124, CT, V01125 CT, V01126, CT, V01127 CT, V01128			
HI	I	315		001117, CT, V01116 001138, 001119 001120, CT, V01121 CT, V01122, CT, V01123 CT, V01124, CT, V01125 CT, V01126, CT, V01127 CT, V01128			
HEM18	O	310		1/26, 1/27 1/28, 1/27 1/28		HEM1	3H2
HSEL008	O	115		TO CONN CKT		HSEL00	3H8
HSEL018	O	015		TO CONN CKT		HSEL01	3H7
HSEL108	O	311		TO CONN CKT		HSEL10	3H1
HSEL108	O	017		TO CONN CKT		HSEL10	3H6
HSEL118	O	116		TO CONN CKT		HSEL11	3H7
HSEL208	O	118		TO CONN CKT		HSEL20	3H7
HSEL118	O	018		TO CONN CKT		HSEL21	3H6
REFPROB	I	214		TO CONN CKT		REF10	3A0
REFOB	O	108		1/16, 1/21, 1/17 1/22, 1/23 (211/28, 1/21, 1/19 (211/28, 1/21, 1/19 (211/28, 1/21, 1/19 1/24, 1/25 1/26, 1/27 1/28		REF0	3H0
HSEL11	O	013		TO CONN CKT		HSEL11	3H1
RM08	I	300		TO CONN CKT		RM10	3A2
SSEL008	O	210		TO CONN CKT		SSEL00	3H3
SSEL108	O	205		TO CONN CKT		SSEL00	3H3
SSEL018	I	113		TO CONN CKT		SSEL11	3A1
SSEL118	I	114		TO CONN CKT		SSEL11	3A1
SSEL018	I	014		TO CONN CKT		SSEL21	3A1
UNACOB	I	313		TO CONN CKT		UNO0118	3H5
UNSEL08	I	314		TO CONN CKT		UNSEL08	3H5
V4	I	312					
02CDH00	GRD	0020				GRD	3H9
	GRD	0020				GRD	3H9
	GRD	200				GRD	3H9
	GRD	319				GRD	3H9
SV	PWR	000				-5	

MEMORY

MEMORY PLANE 1
DATA BITS PH AND PL

```
----- FS INFO ----- CP INFO -----
```

MEMORY PLANE 1
DATA BITS 23 AND 19

----- ES INFO ----- CB INFO -----

MEMORY PLANE 1
DATA BITS 32 AND 16

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MEMORY PLANE 1
0x0 0x100 0x200 0x300

12V	GRD 219 PWR 000	GRD 3A0 +12 3F0
-----	--------------------	--------------------

12V	GRD	219	GRD	3A0
	PWR	000	+12	3E0

12V	GRD	219	GRD	3A0
	PWR	000	+12	3E0

GRD	219	GRD	3A0
DUP	000	DUP	750

MAIN STORE MEMOR

DW ⁺ S
C2

ISSUE
20

BELL LABORATORIES	SD-10903-02
-------------------	-------------

R1CF

PRINTED IN U. S. A.

R1CF

MEMORY

MEMORY PLANE 1
DATA BITS 20 AND 16

MEMORY PLANE 1
DATA BITS 15 AND 07

MEMORY PLANE 1
DATA BITS 14 AND 06

MEMORY PLANE 1
DATA BITS 13 AND 05

9

D4.22

D4.22

81CG

087027

...and ...

MEMORY

MEMORY PLANE 1
DATA BITS 12 AND 04

MEMORY PLANE 1
DATA BITS 11 AND 03

MEMORY PLANE 1
DATA BITS 10 AND 02

MEMORY PLANE 1
DATA BITS 09 AND 01

SYMBOL(S) 24 25 26 27

MAIN STORE MEMORY		DWG SIZE C2	ISSUE 2D
BELL LABORATORIES	SD-1C903-02	81CH	

PART OF FS 1

MEMORY

SYMBOL NO. 28

MEMORY PLANE 1
DATA BITS 08 AND 00

DESIG	EOPT LOC	CODE	ELEN TOENT	OPT
RP108.00	03-31	JL2	A	

FS INFO

CP INFO

LEAD DESIG	FUNC	TERM.	DESTINATION	NOTE	TERM. IND	LOC
-SV	PWR	019			-5	380
CEA01B	I	202	1/15		CEA011	200
CEA11B	I	009	1/15		CEA111	200
CEA21B	I	209	1/15		CEA211	220
CEA00B	I	206	1/15		CA010	2F0
CEA10B	I	204	1/15		CA110	2F0
CEA30B	I	004	1/15		CA210	2F0
CEA30B	I	008	1/15		CA310	2F0
CEA40B	I	005	1/15		CA410	2F0
CEA50B	I	207	1/15		CA510	2F0
CEA60B	I	003	1/15		CE10	2F0
CEA60B	I	007	1/15		RA010	2H0
GRA10B	I	205	1/15		RA110	2H0
GRA20B	I	006	1/15		RA210	2H0
GRA30B	I	203	1/15		RA310	2H0
GRA40B	I	201	1/15		RA410	2G0
GRA50B	I	002	1/15		RA510	2G0
GR0A	GRD	219	2/1		GRD	3A0
GR0B	I	001	1/15		REN1	2A9
HE	I	018	(T,V)1/15			
HE	I	017	(T,V)1/15			
HG	I	016	(T,V)1/15			
HE1	I	015	(T,V)1/15			
HE1B	I	014	1/15			
RO000B	O	214	TO CONN CKT		RO000	2G8
RO000B	O	212	TO CONN CKT		RO010	2G9
REF0B	I	208	1/15		REF10	2E0
WRT0000B	I	218	TO CONN CKT		WRT0010	2A3
WRT0000B	I	216	TO CONN CKT		WRT0110	2A9
WRT0000B	GRD	050			GRD	300
GRD	GRD	250			GRD	300
GRD	GRD	208			GRD	300
GRD	GRD	213			GRD	254
GRD	GRD	215			GRD	2G8
GRD	GRD	217			GRD	2A8
GRD	GRD	219			GRD	3A0
12V	PWR	000			+12	3F0
5V	PWR	011			+5	3C0
	PWR	211			+5	3C0

PART OF FS-1
SYMBOL(S) 28

MAIN STORE MEMORY		OWN SIZE: C2	ISSUE 2D
BELL LABORATORIES		SD-1C903-02	
		B1CJ	

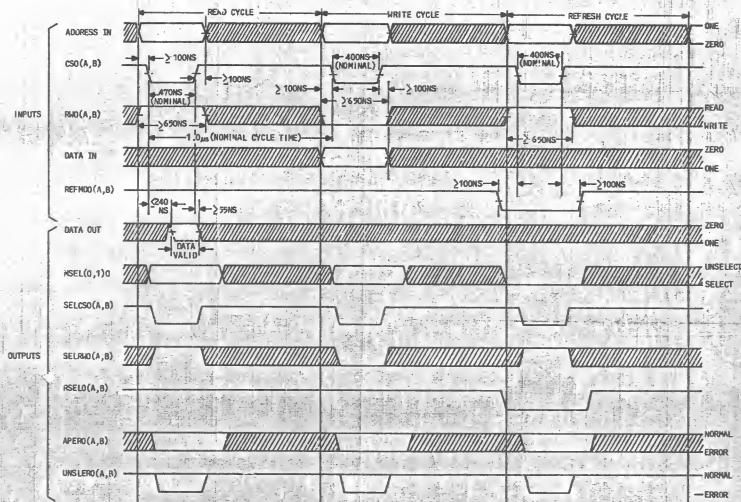
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PART OF FS I

MEMORY
COMPOSITE DIAGRAM 1
TYPICAL TIMING RELATIONSHIPS

NOTES:

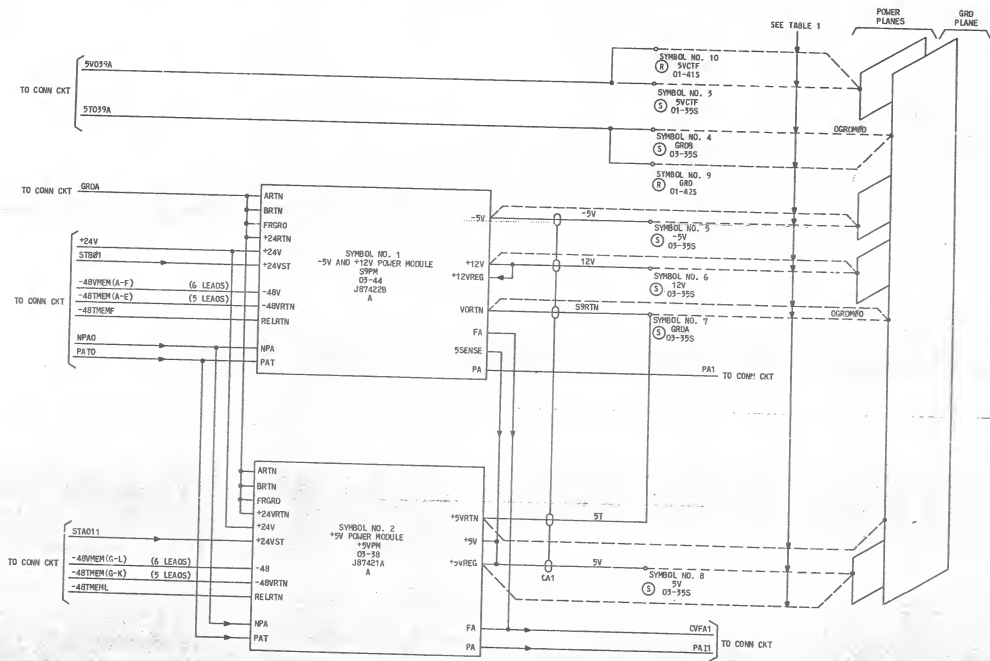
1. BECAUSE OF THE ASYNCHRONOUS NATURE OF THE WASH CIRCUIT, MOST OF THE TIMING AND LEVEL CONSTRAINTS WILL BE DETERMINED BY SPECIFIC APPLICATION REQUIREMENTS. THE INFORMATION PRESENTED HERE IS INTENDED AS A TYPICAL FUNCTIONAL GUIDE ONLY.
2. TIMING MEASUREMENTS ARE REFERENCED TO 1.5V POINTS.
3. LOW INPUT VOLTAGE LEVEL - $0V \leq V \leq 0.8V$.
4. HIGH INPUT VOLTAGE LEVEL - $2.4V \leq V \leq 5.0V$.
5. INPUT RISE AND FALL TIMES ARE $\leq 10NS$.
6. OUTPUTS ARE OPEN COLLECTOR. CHARACTERISTICS ARE DETERMINED BY THE PULLUP NETWORK USED.
7. SHADED AREAS  REPRESENT "DON'T CARE" STATES.



PART OF FS I
COMPOSITE DIAGRAM 1

MAIN STORE MEMORY		DATA SIZE 65	ISSUE 1
BELL LABORATORIES	30-10903-02	BIGA	

PART OF FS 2
- MEMORY POWER
INTERCONNECTION FLOW DIAGRAM



POWER DISTRIBUTION_TABLE_1

CIRCUIT PACK	EPT LOC	GRID PLANE	-5V	-9V	+12V
		TERM	TERM	TERM	TERM
JK3	05-02	200, 319 000, 250	000, 119		
J12	05-04	200, 000, 250, 215, 215, 217, 219	011, 215	019	000
	05-05				
	05-06				
	05-07				
	05-08				
	05-09				
	05-10				
	05-11				
	05-12 (2)	(2)	(2)	(2)	(2)
	05-13 (2)	(2)	(2)	(2)	(2)
	05-14 (2)	(2)	(2)	(2)	(2)
	05-16 (2)	(2)	(2)	(2)	(2)
	05-17				
	05-18				
	05-19 (2)	(2)	(2)	(2)	(2)
	05-20 (2)	(2)	(2)	(2)	(2)
	05-21 (2)	(2)	(2)	(2)	(2)
	05-22 (2)	(2)	(2)	(2)	(2)
	05-23				
	05-24				
	05-25				
	05-26				
	05-27				
	05-28				
	05-29				
J12	05-31	200 000, 250, 215, 215, 217, 219	011, 211	019	000
JK3	05-33	200, 319 000, 250	000, 119		
J87421A	05-38				
J87422B	05-44				

PART OF FS 2
INTERCONNECTION AND FLOW DIAGRAM

MAIN STORE MEMORY		2	DWG SIZE A5	ISSUE 2D
BELL LABORATORIES	SD-IC903-02		B2AA	

PART OF FS 2

MEMORY POWER

SYMBOL/LEAD DESIGNATION

SYMBOL	LEAD DESIGNATION
MEMORIC	DEFINITION
+24V	+24 VOLT FRAME POWER
+48VEMKA-L1	+48 VOLT RETURN ON MEMORY MODULE
+48VEMKA-L1	+48 VOLT (POWER) ON MEMORY MODULE
-5V	-5 VOLT (MEMORY POWER)
CVFA1	CONVERTER FUSE ALARM
GRDA	GROUND
NPAD	NEGATIVE POWER ALARM (POWER ALARM STATUS AND RESET FOR CONVERTERS)
PA11	POWER ALARM INPUT
PAT0	POWER ALARM TEST
PA1	POWER ALARM
STAD11	START A
STB1	START B
SVRTN	SV RETURN (GROUND)
CGRMD0	GROUND
12V	+12 VOLT (MEMORY POWER)
5T	+5V RETURN (TO MEMORY)
ST039A	+5V RETURN (TO TERMINATIONS)
5V	+5 VOLT (MEMORY POWER)
SV039A	+5 VOLT (TO TERMINATIONS)

MAIN STORE MEMORY		DRG SIZE	ISSUE
		C2	1
BELL LABORATORIES	SD-1C903-02		8248

6072876

MEMORY POWER

MEMORY POWER

-5V AND +12V POWER MODULE

+5V POWER MODULE

+5V POWER MODULE

1000

DESIG 12V	EPPT LOC 03-35S	CODE 840851786	ELEM IDENT	OPT (S)
FS INFO				
LEAD DESIG 12V	FUNC PAR	TERM. 12V	DESTINATION 2/1	NOTE
CP INFO				
LEAD DESIG 12V	FUNC PAR	TERM. 12V	DESTINATION 2/1	NOTE
SYMBOL NO. 7				
DESIG GRDA	EPPT LOC 03-35S	CODE 840851786	ELEM IDENT	OPT (S)
FS INFO				
LEAD DESIG 5V	FUNC GRD	TERM. GRD	DESTINATION 2/2	NOTE
CP INFO				
LEAD DESIG 5V	FUNC GRD	TERM. GRD	DESTINATION 2/2	NOTE
SYMBOL NO. 8				
DESIG 5V	EPPT LOC 03-35S	CODE 840851786	ELEM IDENT	OPT (S)
FS INFO				
LEAD DESIG 5V	FUNC PAR	TERM. 5V	DESTINATION 2/2	NOTE
CP INFO				
LEAD DESIG 5V	FUNC PAR	TERM. 5V	DESTINATION 2/2	NOTE

PART OF FS 2

SYMBOL(S) 1 2 3 4 5 6 7 8

2. $\lim_{x \rightarrow 0} \frac{1}{x} = \infty$ (or $-\infty$)

Table 1

100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120

Table 1

PART OF FS 2

SYMBOL(S) 1 2 3 4 5 6 7 8

HALL STORE REPORT		DMG SIZE CZ	ISSUE 2D
SELL LABORATORIES	SD-1 C903-02	B2CA	

PART OF FS 2

MEMORY POWER

SYMBOL NO. 9

DESIG	EGPT	CODE	ELEM	OPT
GRD	LOC		IDENT	
	01-425	840851786		(R)

FS INFO

CP INFO

LEAD	FUNC	TERM.	DESTINATION	NOTE	TERM.	LOC
DESIG					MOD	
6060000	GRD	GRD				
57039A	GRD	GRD	(R) TO COMM CKT			

SYMBOL NO. 10

DESIG	EGPT	CODE	ELEM	OPT
SVCTF	LOC		IDENT	
	01-415	840851786		(R)

FS INFO

CP INFO

LEAD	FUNC	TERM.	DESTINATION	NOTE	TERM.	LOC
DESIG					MOD	
5V039A	PMR	SVCTF	(R) TO COMM CKT			

PART OF FS 2
SYMBOL(S) 9 10

MAIN STORE MEMORY		DWG SIZE	ISSUE
		C2	2D
BELL LABORATORIES	SD-1C903-02	82CB	
PRINTED IN U. S. A.		57/02777	

A —
B —
C —
D —
E —
F —
G —
H —

OPTION	DESIG	FS/SYM	CODE
	+SVPM	2/2	J87421A
	S9PM	2/1	J87422B

A vertical scale with labels A, B, C, D, E, F, G, and H. A horizontal line with a dot is drawn at the level of label D.

[illegible]

F
—
G
—
H

[illegible]

SD-1C903-02-C1

BELL TELEPHONE LABORATORIES
INCORPORATED

CMG 512
C2

APP FIG. 4

ENG 5126
C3

APP FIG. 5

SD-102-C2

(CIRCUIT NOTES) (CONT)

104. TO PROVIDE ADDRESS IDENTITY FOR MEMORY MODULES, PROVIDE 30 GA., 03-TYPE MODULE SELECT STRIPS AT THE ASSOCIATED FANOUT BOARD TERMINALS AS SHOWN IN THE ACCOMPANYING TABLE (THREE WIRES PER MODULE).

DESIRED MODULE ADDRESS	WIRE FANOUT BOARD TERMINALS		
	014 105	114 105	115 105
0	118	017	115
1	118	017	015
2	118	116	115
3	118	116	015
4	018	017	115
5	018	017	015
6	018	116	115
7	018	116	015

105. PRIOR TO ISSUE 20, OPTION Z WIRING WAS ALWAYS FURNISHED.

EQUIPMENT NOTES:

201. PRINTED WIRING PLANES OR PATHS ARE LOCATED IN MULTILAYER PRINTED WIRING BOARD, ED-AC091-30. PRIOR TO ISSUE 2 PRINTED WIRING PLANES OR PATHS ARE LOCATED IN MULTILAYER PRINTED WIRING BOARD, ED-AC005-30.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED: VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.

CIRCUIT NOTES:

DESIG	FUSE AMP	POTENTIAL	ONE PER
+24V	3/4	+24	APP FIG. 1
-48VDC(A-L)	3	-48	APP FIG. 1
BATTERY SYMBOL			
+24		VOLTAGE RANGE	
-48		20, 75-24, 25 42, 75-52, 5	

- * - UNIT LEADS SHALL BE MECHANICALLY INTERCONNECTED ON USER'S CONNECTING CIRCUIT VIA 9420 TYPE BEND CONNECTOR OR EQUIVALENT.

FEATURE OR OPTION	PROVIDE	
	APP FIG.	QUANTITY
MAIN STORE POWER MODES AND ALL MEMORY LIRING FOR 18-BIT STORE WORD	1	1 PER CKT
32, 768 WORDS BY 18 BITS	2	1 PER CKT
32, 768 WORDS BY 26 BITS	2, 3	1 PER CKT
(CAPACITY) 65, 536 WORDS BY 18 BITS	2, 4	1 PER CKT
65, 536 WORDS BY 26 BITS	2-5	1 PER CKT
ADDITIONAL WIRING FOR 26-BIT STORE	Z	
ADDITIONAL PJ	Y	
IN ED-AC091-30	18-BIT STORE	Y
WIRING DIFFERENCES	ED-AC005-30	S
	ED-AC091-30	H

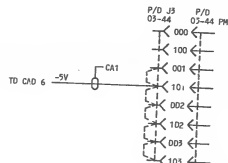
RECORD OF FIGURES, WIRING AND APPARATUS CHANGES						
CHANGED ON ISSUE	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION HAS SPECIFY	SEE NOTE	USE IN CIRCUIT		
				STD	ADM	NO
20	Y OR Z	Z	105	Y AND Z		
20	T OR V	NONE		T AND V		
20	R OR S	S		H		S

MAIN STORE MEMORY		2	DWG SIZE	15"X6
			65	2D
BELL LABORATORIES		SD-10903-02		DI

CAD NOTES:

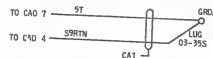
1. THE FOLLOWING SHOWS THE SYMBOLIC EQUIVALENT OF THE TABULAR REPRESENTATION.

TO CONNECTION				FROM CONNECTION			
DESTINATION	LEAD DESIG	WIRE METHOD	SYN	TERMINAL	LEAD DESIG	TO TERMINATION	TERMINAL DPT NOTE
.....J3	J3	J3	J3	05-44 JACK/PH	05-44 JACK/PH	(NOTE 1).....	
-5V	NC	NC	NC	000	-5V		
-5V	NC	NC	NC	001	-5V		
-5V	NC	NC	NC	002	-5V		
TO CAD 6	CA1	CA1	CA1	100	-5V		
-5V	NC	NC	NC	101	-5V		
-5V	NC	NC	NC	102	-5V		
-5V	NC	NC	NC	103	-5V		



2. THE FOLLOWING SHOWS THE SYMBOLIC EQUIVALENT OF THE TABULAR REPRESENTATION.

TO CONNECTION				FROM CONNECTION			
DESTINATION	LEAD DESIG	WIRE METHOD	SYN	TERMINAL	LEAD DESIG	TO TERMINATION	TERMINAL DPT NOTE
.....GROA	GROA	GROA	GROA	05-355 LUG GRO/PH	05-355 LUG GRO/PH	(NOTE 2).....	
TO CAD 7	ST	CA1	CA1	000	-5V		
TO CAD 4	SWRTN	CA1	CA1	001	-5V		



- WIRE SHALL BE 18 GA, KS-13305 STRANDED.
- WIRE SHALL BE 16 GA, KS-13305 STRANDED.
- WIRE METHOD CA1 IS DEFINED AS BEING PART OF ED-AC053-30 CABLE ASSEMBLY.
- THE FOLLOWING CODES OF CONNECTORS ARE USED:

J1 94SAJ
J2 94SAN
J3 94SAK
J4 94SAJ

MAIN STORE INVENTORY		2		DWG SIZE		ISSUE	
BELL LABORATORIES		SD-1C903-02		AS		1	
						681	

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

MEMORY CIRCUIT INTERFACE

OWG SIZE	ISSUE
F2	20

27	
----	--

RAIN STORE MEMORY		ONG SIZE C2	ISSUE 2D
BELL LABORATORIES	SD-1C903-02		GB3

CAD 2

CAD 3

CAD 5

(FOR USE WITH ED-4C005-30, OPTION 5)

(CONT'D)

TO CONNECTION										FROM CONNECTION										TO CONNECTION										FROM CONNECTION									
DESTINATION	LEAD DESIG	METHOD	WIRE SYN	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE	DESTINATION	LEAD DESIG	METHOD	WIRE SYN	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE	DESTINATION	LEAD DESIG	METHOD	WIRE SYN	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE	DESTINATION	LEAD DESIG	METHOD	WIRE SYN	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL	OPT	NOTE
.....	SV039A			01-00	CTF					J1	03-38	JACK/PH		(NOTE 13).....	J3	03-44	JACK/PH		(NOTE 13).....	J3	03-44	JACK/PH		(NOTE 13).....	J3	03-44	JACK/PH		(NOTE 13).....	J3	03-44	JACK/PH		(NOTE 13).....	J3	03-44	JACK/PH		(NOTE 13).....
.....	SV039A			30	SV039A						5V	RC	000	5V		100	5V			TO CAD 6	-5V	CA1	100	-5V		100	-5V				-5V	CA1	100	-5V		100	-5V		
.....	SV039A			32	SV039A						5V	RC	001	5V		101	5V				-5V	RC	101	-5V		101	-5V				-5V	RC	101	-5V		101	-5V		
.....	01-08	CTF									5V	RC	002	5V		102	5V				-5V	RC	102	-5V		102	-5V				-5V	RC	102	-5V		102	-5V		
.....	SV039A			20	SV039A						5V	RC	003	5V		103	5V				12V	RC	103	12V		103	12V				12V	RC	103	12V		103	12V		
.....	0000103T			23	0000103T						5V	RC	004	5V		104	5V				12V	RC	104	12V		104	12V				12V	RC	104	12V		104	12V		
.....	01-06	CTF								TO CAD 6	5V	CA1	005	5V		105	5V				12V	RC	105	12V		105	12V				12V	RC	105	12V		105	12V		
.....	SV039A			10	SV039A						5V	RC	006	5V		106	5V				12V	RC	106	12V		106	12V				12V	RC	106	12V		106	12V		
.....	01-09	CTF									5V	RC	007	5V		107	5V				12V	RC	107	12V		107	12V				12V	RC	107	12V		107	12V		
.....	SV039A			00	SV039A						5V	RC	008	5V		108	5V				12V	RC	108	12V		108	12V				12V	RC	108	12V		108	12V		
.....	01-11	CTF									5V	RC	009	5V		109	5V				12V	RC	109	12V		109	12V				12V	RC	109	12V		109	12V		
.....	SV039A			30	SV039A						5V	RC	010	5V		110	5V				12V	RC	110	12V		110	12V				12V	RC	110	12V		110	12V		
.....	01-12	CTF									5V	RC	011	5V		111	5V				12V	RC	111	12V		111	12V				12V	RC	111	12V		111	12V		
.....	V1			10	V1						5V	RC	012	5V		112	5V				12V	RC	112	12V		112	12V				12V	RC	112	12V		112	12V		
.....	V2			30	V2						5V	RC	013	5V		113	5V				12V	RC	113	12V		113	12V				12V	RC	113	12V		113	12V		
.....	01-14	CTF									5V	RC	014	5V		114	5V				12V	RC	114	12V		114	12V				12V	RC	114	12V		114	12V		
.....	SV039A			20	SV039A						5V	RC	015	5V		115	5V				12V	RC	115	12V		115	12V				12V	RC	115	12V		115	12V		
.....	01-18	CTF									5V	RC	016	5V		116	5V				12V	RC	116	12V		116	12V				12V	RC	116	12V		116	12V		
.....	SV039A			30	SV039A						5V	RC	017	5V		117	5V				12V	RC	117	12V		117	12V				12V	RC	117	12V		117	12V		
.....	01-21	CTF									5V	RC	018	5V		118	5V				12V	RC	118	12V		118	12V				12V	RC	118	12V		118	12V		
.....	SV039A			20	SV039A						5V	RC	019	5V		119	5V				12V	RC	119	12V		119	12V				12V	RC	119	12V		119	12V		
.....	01-24	CTF									5V	RC	020	5V		120	5V				12V	RC	120	12V		120	12V				12V	RC	120	12V		120	12V		
.....	SV039A			10	SV039A						5V	RC	021	5V		121	5V				12V	RC	121	12V		121	12V				12V	RC	121	12V		121	12V		
.....	01-22	CTF									5V	RC	022	5V		122	5V				12V	RC	122	12V		122	12V				12V	RC	122	12V		122	12V		
.....	SV039A			00	SV039A						5V	RC	023	5V		123	5V				12V	RC	123	12V		123	12V				12V	RC	123	12V		123	12V		
.....	01-29	CTF									5V	RC	024	5V		124	5V				12V	RC	124	12V		124	12V				12V	RC	124	12V		124	12V		
.....	SV03CA			30	SV03CA						5V	RC	025	5V		125	5V				12V	RC	125	12V		125	12V				12V	RC	125	12V		125	12V		
.....	01-32	CTF									5V	RC	026	5V		126	5V				12V	RC	126	12V		126	12V				12V	RC	126	12V		126	12V		
.....	SV039A			20	SV039A						5V	RC	027	5V		127	5V				12V	RC	127	12V		127	12V				12V	RC	127	12V		127	12V		
.....	SV039A			22	SV039A						5V	RC	028	5V		128	5V				12V	RC	128	12V		128	12V				12V	RC	128	12V		128	12V		
.....	01-35	CTF									5V	RC	029	5V		129	5V				12V	RC	129	12V		129	12V				12V	RC	129	12V		129	12V		
.....	V4			20	V4						5V	RC	030	5V		130	5V				12V	RC	130	12V		130	12V				12V	RC	130	12V		130	12V		
.....	V3			30	V3						5V	RC	031	5V		131	5V				12V	RC	131	12V		131	12V				12V	RC	131	12V		131	12V		
.....	01-12	CTF									5V	RC	032	5V		132	5V				12V	RC	132	12V		132	12V				12V	RC	132	12V		132	12V		
.....	V1			17	V1						5V	RC	033	5V		133	5V				12V	RC	133	12V		133	12V				12V	RC	133	12V		133	12V		
.....	V2			37	V2						5V	RC	034	5V		134	5V				12V	RC	134	12V		134	12V				12V	RC	134	12V		134	12V		
.....	01-35	CTF									5V	RC	035	5V		135	5V				12V	RC	135	12V		135	12V				12V	RC	135	12V		135	12V		
.....	V4			27	V4						5V	RC	036	5V		136	5V				12V	RC	136	12V		136	12V				12V	RC	136	12V		136	12V		
.....	V3			37	V3						5V	RC	037	5V		137	5V				12V	RC	137	12V		137	12V				12V	RC	137	12V		137	12V		
.....	01-35	CTF									5V	RC	038	5V		138	5V				12V	RC	138	12V		138	12V				12V	RC	138	12V		138	12V		
.....	SV039A			20	SV039A						5V	RC	039	5V																									

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(FOR USE WITH ED-4C095-30, OPTION 5)

TO CONNECTION				FROM CONNECTION			
DESTINATION	LEAD DESIG	METHOD	WIRE STR	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL
.....44				01-38	JACK/PH	(NOTE 1)	
44	HC			200			
5T	HC			201			5T
5T	HC			202			5T
5T	HC			203			5T
5T	HC			204			5T
5T	HC			205			5T
5T	HC			206			5T
5T	HC			207			5T
5T	HC			208			5T
5T	HC			209			5T
5T	HC			300			5T
5T	HC			301			5T
5T	HC			302			5T
5T	HC			303			5T
5T	HC			304			5T
5T	HC			305			5T
5T	HC			306			5T
5T	HC			307			5T
5T	HC			308			5T
5T	HC			309			5T

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(FOR ED-4C091-30)

TO CONNECTION				FROM CONNECTION			
DESTINATION	LEAD DESIG	METHOD	WIRE STR	TERMINAL	LEAD DESIG	TERMINATION	TERMINAL
.....SVCTF				01-415	LUG	(NOTE 2)	
(R) TO CONN DKT 5V039A				SVCTF	5V039A		R
.....GRD				01-425	LUG	(NOTE 2)	
(R) TO CONN DKT 5T039A				GRD	00RDPOD		R

MAIN STORE MEMORY		DWG SIZE	ISSUE
		12	20
BELL LABORATORIES	SD-1C903-02	GB5	
		06/02/77	